



October 27, 2026

IUST to host 2-day National Workshop on Semiconductors, Quantum Materials and Deep Technology

Awantipora, Oct 27: The Islamic University of Science and Technology (IUST), through its Centre of Excellence for Semiconductors and Quantum Materials (CoESQM), will organise a two-day national workshop on Semiconductors, Quantum Materials, and Deep Technology on October 28-29 at the University campus.

The workshop aims to bring together eminent scientists, academicians, industry experts, researchers, and students to deliberate on emerging developments in semiconductor technology, quantum materials, chip design and manufacturing, and other strategic areas of deep technology.

The inaugural session will be presided over by Prof. Shakil A Romshoo, Vice-Chancellor, IUST, as the Chief Guest. Prof. H. S. Jatana, former Scientist-G and Group Head, Design and Process Group, Semiconductor Laboratory (SCL), Mohali; Mr. Asim Khan, Director, Software Technology Parks of India (STPI), Srinagar; and Prof. Pragati Kumar, Vice-Chancellor, Shri Mata Vaishno Devi University (SMVDU), will be the Guests of Honour.

IUST has been strengthening its academic and research engagement in semiconductors and quantum materials by promoting interdisciplinary collaboration and creating opportunities for students and researchers to engage with emerging technologies critical to India's technological advancement. The University has recently established the Centre of Excellence for Semiconductors and Quantum Materials (CoESQM) to build research and training capabilities in semiconductor technologies, quantum materials, and chip design, while fostering interdisciplinary collaboration and engagement with industry.

The workshop will feature technical sessions on semiconductor materials and fabrication, quantum materials, semiconductor devices and manufacturing, testing and reliability, quantum computing, and the policy, funding, and regulatory landscape of emerging technologies. The programme will also include expert lectures and a panel discussion titled "From Lab to Fab: Advancing India's Semiconductor and Quantum Ecosystem for Viksit Bharat@2047."

The panel discussion will feature Prof. H. S. Jatana, former Scientist-G and Group Head, Design and Process Group, SCL Mohali; Mr. PVG Menon, former Vice-President and Head of Business Development at Tata Projects Limited and Senior Advisor to the FAB Project at IndiChip Semiconductors Limited; Prof. Alex James, IEEE CASS Board of Governors, Maker Village, and Semicon Kerala; Mr. S. Uma Mahesh, Co-founder and Managing Director of Innatera Nanosystems India Pvt. Ltd. and associated with Innatera Nanosystems B.V. and SFAL; and Dr. Rukhsan Ul Haq, CEO and Chief Scientist, Chinara Quantum AI, Srinagar.

The workshop will feature contributions from experts associated with premier institutions, including the Indian Institutes of Technology (IITs), the Institute of Mathematical Sciences, Chennai, the Indian Institute of Science Education and Research (IISER), the Semiconductor Laboratory, Mohali, and other national and international research and industry organizations.

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