



One Week Faculty Development Program

Semiconductor Devices, fabrication and Layout

conducted by
Department of Electronics and Communication Engineering
IUST Kashmir.

In collaboration with ICT Academy Tamil Nadu, India

Sponsored by
"Ministry of Electronics and Information Technology (MeitY), Govt of India"



Chief patron: Prof. Shakeel Ahmad Romsho
IUST Kashmir.

Patron: Prof. A.H Moon,
Dean Acad. Affairs, IUST



Convenor: Dr. Feroze Ahmed, Head ECE, IUST.

Organised by
Kalam Academy of Skill Development and Training,
Islamic University of Science & Technology, Kashmir.

Date: 09 March 26 - 13 March 26

Registration Fee: NIL
Scan to register



Register Link: https://ictacademy.in/projects/MeityPhase2_FacultyEnroll.aspx?Batchid=2fEPBMiws170-KRa4cN8tw

Who can participate:

FDPs will cover critical areas aligned with existing trades, including:

- Electrical Engineering
- Electronics & Communication Engineering
- Electronics and Instrumentation Engineering

FDP Outcome

The FDP is aimed to bring together faculty, researchers & users to discuss about advancements, and offer hands-on session in semiconductor device, fabrication and layout design tools.

Expected Tools to be covered

Magic, Klayout
Ngspice, IC validator
Cadence Virtuoso.



Venue: First Floor Academic Block-III,
Main Campus IUST, Kashmir

Magic VLSI Layout Tool

Current distribution version 8.3

XSCHEM

Mode of the faculty development program: Offline.
Resource persons will be from industry and practitioners from relevant fields

Certificates, LUNCH/ IFTAAR shall be provided to the participants

For certification:

Participants who have 90% or above attendance and scored 60% or above in all the assessments will be eligible for the certification.

FDP Coordinator
Dr. Romana Yousuf, A.P. ECE, IUST
Contact: 7006600741

For training-related queries, you can contact:
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