



Department of Electronics and Communication Engineering
Islamic University of Science & Technology, Kashmir.

Press Release

FDP on Semiconductor Devices, Fabrication and Layout Design Concludes at Islamic University of Science and Technology, Kashmir

The Department of Electronics and Communication Engineering at Islamic University of Science and Technology (IUST), Kashmir, successfully conducted a one week Faculty Development Programme (FDP) on “Semiconductor Devices, Fabrication and Layout Design” from 09 March 2026, conducted under the Faculty Development Programme in ESDM initiative of the Ministry of Electronics and Information Technology (MeitY) in collaboration with ICT Academy, under the aegis of the Kalam Academy of Science and Technology (KAST).

The inaugural session was graced by Prof. A. H. Moon, Dean Academic Affairs, who highlighted the growing scope and opportunities in the semiconductor industry. Dr. Kaiser J. Giri, Dean School of Engineering and Technology, emphasized the role of IUST in promoting skill-based education and strengthening technical competencies. Also present were Dr. Faroze Ahmad, Head of the Department of Electronics and Communication Engineering, who delivered the opening remarks and welcomed the participants. Dr. Romana Yousuf, coordinator of the event, Dr. Liyaqat Nazir, Coordinator of KAST, along with faculty members from the department of ECE and participants from various polytechnics and it is were also present. The session concluded with a vote of thanks presented by Dr. Khalid Muzzafar.

The FDP was designed to enhance the conceptual understanding and practical competencies of participants in the domains of semiconductor devices, fabrication processes, and layout design. The sessions were conducted by resource person Mr. Peddi Raju, who delivered expert lectures and facilitated extensive hands-on training on semiconductor device design and fabrication technologies.

A major highlight of the programme was the practical training on Cadence tools, where participants gained hands-on experience in circuit design, simulation, and layout development, offering valuable exposure to industry-oriented semiconductor design practices.

The FDP concluded on 13 March 2026, with the department expressing sincere gratitude to the Ministry of Electronics and Information Technology, ICT Academy, KAST, the resource person, and all participants for their valuable support and contribution to the successful conduct of the programme.